



501.37370CC4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Naofumi OHASHI *et al.*
Application No.: 10/760,292
Filed: January 21, 2004
For: PROCESS FOR MANUFACTURING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Expected Group: 2824
Expected Examiner: M. Lebentritt

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 5, 2004

Sir:

Please amend the above-identified application, prior to examination thereof,
as listed below, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.